

RYTQC40-4

Features

- Supports 103Gbps
- Single 3.3V Power Supply and Power dissipation < 4.5W
- Up to 30km reach for G.652 SMF without FEC
- Up to 40km reach for G.652 SMF with FEC
- RoHS-6 compliant (lead-free)
- Commercial case temperature: 0°C to 70°C
- Four 25Gbps EML LAN-WDM channels on transmitter side
- APD and TIA array on the receiver side
- 4x25G electrical interface
- Duplex LC receptacles
- I²C interface with integrated Digital Diagnostic Monitoring

Applications

- 100GBASE-ER4 100G Ethernet

Description

RYTQC40-4 QSFP28 transceiver module is designed for 103 Gigabit Ethernet links over 40Km single mode fiber. It is compliant with IEEE 802.3ba 100GBASE-ER4 LITE . Digital diagnostics functions are available via an I²C interface, as specified by the QSFP+MSA.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T _s	-40	+85	°C
Supply Voltage	V _{cc}	-0.5	3.6	V
Operating Relative Humidity	RH	5	85	%

Exceeding any one of these values may destroy the device immediately

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	T _c	0		70	°C
Power Supply Voltage	V _{cc}	3.135	3.3	3.465	V
Power Dissipation	P _D			3.5	W

Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Transmitter Section:						
Input Amplitude (Differential)		150		1200	mV	□
Input Impedance (Differential)	Zin	85	100	115	ohms	Rin > 100 kohms @ DC
Receiver Section:						
Output Amplitude (Differential)		200		1100	mV	
Output Impedance (Differential)	Zout	85	100	115	ohms	
Output Rise/Fall Time	t _r /t _f		12		ps	10%~90%

Optical Characteristics

100GBASE-ER4 Operation

Parameter	Symbol	Min.	Typical	Max.	Unit
Transmitter Section:					
Signaling Speed per Lane	BR _{AVE}		25.78		Gbps
Data Rate Variation		-100		+100	ppm
Lane_0 Center Wavelength	λ _{C0}	1294.53	1295.56	1296.59	nm
Lane_1 Center Wavelength	λ _{C1}	1299.02	1300.05	1301.09	nm
Lane_2 Center Wavelength	λ _{C2}	1303.54	1304.58	1305.63	nm
Lane_3 Center Wavelength	λ _{C3}	1308.09	1309.14	1310.19	nm
Total Average Output Power ^{*(note1)}	P _o			10.5	dBm
Average Launch Power per Lane	P _{each}	-3		5.2	dBm
Average launch power of OFF transmitter per lane				-30	dBm
Optical modulation amplitude	P _{oma}	-1			dBm
Optical Return Loss Tolerance				20	dB
Extinction Ratio	ER	4			dB
Transmitter eye mask definition {X1, X2, X3, Y1, Y2,Y3} ^{*(note2)}		Note2			
Receiver Section:					
Signaling Speed per Lane	BR _{AVE}		25.78		Gbps
Data Rate Variation		-100		+100	ppm
Damage threshold	R _{dam}	4.5			dBm
Lane_0 Center Wavelength	λ _{C0}	1294.53	1295.56	1296.59	nm
Lane_1 Center Wavelength	λ _{C1}	1299.02	1300.05	1301.09	nm

Lane_2 Center Wavelength	λ_{C2}	1303.54	1304.58	1305.63	nm
Lane_3 Center Wavelength	λ_{C3}	1308.09	1309.14	1310.19	nm
Average Receive Power per Lane	Rpow	-20.5		-4.0	dBm
Receive Sensitivity in OMA per Lane ^{*(note3)}	Pmins			-18.5	dBm
Optical Return Loss	ORL			-26	dBm
LOS Assert	LOSA	-35			dB
LOS De-Assert	LOSD			-24	dBm
LOS Hysteresis		0.5			dBm

Note1: Output is coupled into a 9/125 μ m single-mode fiber.

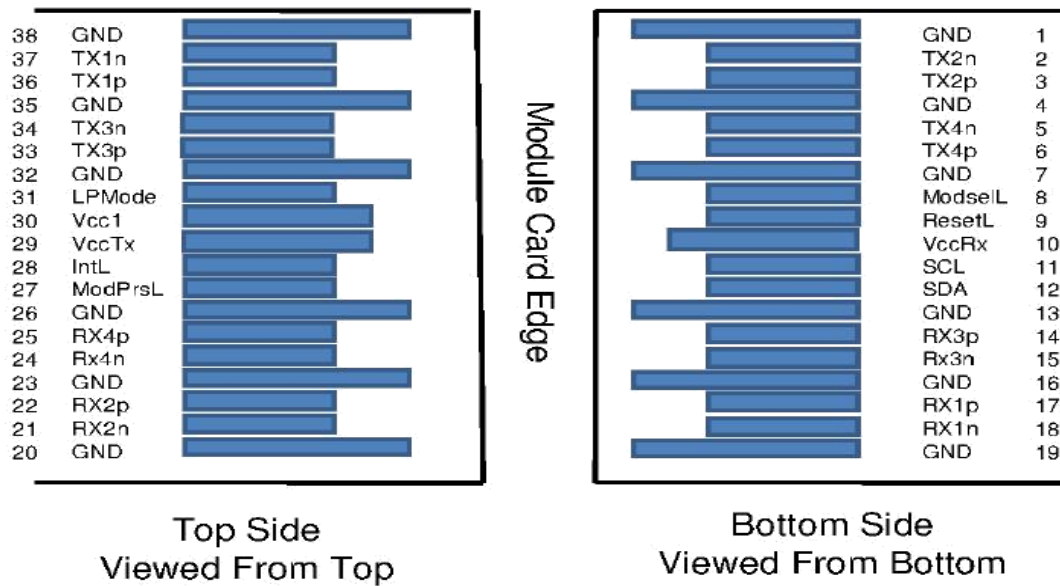
Note2: Eye mask margin is not guaranteed dual to DML laser, still could run 10km single-mode fiber over all temperature.

Note3: Minimum average optical power measured at BER less than 1E-12, with a 2³¹-1 PRBS.

Note4: Output is coupled into a 9/125 μ m single-mode fiber.

Note5: Minimum average optical power measured at BER less than 1E-12, with a 2³¹-1 PRBS.

QSFP28 Transceiver Electrical Pad Layout



Pin Arrangement and Definition

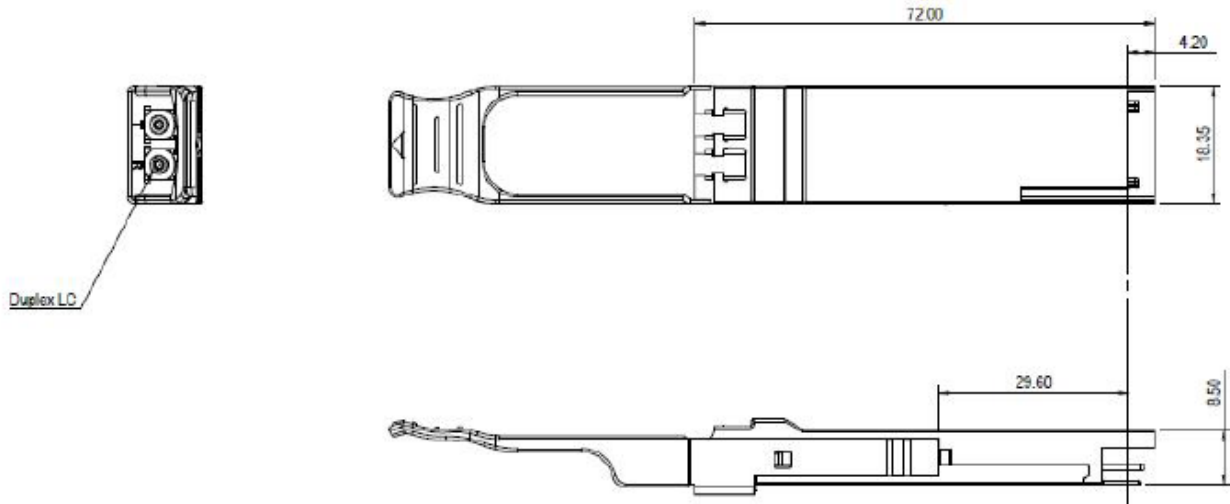
Pin	Logic	Symbol	Description	Plug Sequence	Notes
1		GND	Ground	1	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3	
4		GND	Ground	1	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3	
7		GND	Ground	1	1
8	LVTTL-I	ModSelL	Module Select	3	
9	LVTTL-I	ResetL	Module Reset	3	
10		VccRx	+3.3V Power Supply Receiver	2	2
11	LVCMOS- I/O	SCL	2-wire serial interface clock	3	
12	LVCMOS- I/O	SDA	2-wire serial interface data	3	
13		GND	Ground	1	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3	
15	CML-O	Rx3n	Receiver Inverted Data Output	3	
16		GND	Ground	1	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3	
18	CML-O	Rx1n	Receiver Inverted Data Output	3	
19		GND	Ground	1	1
20		GND	Ground	1	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3	
23		GND	Ground	1	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3	
26		GND	Ground	1	1
27	LVTTL-O	ModPrsL	Module Present	3	
28	LVTTL-O	IntL	Interrupt	3	
29		VccTx	+3.3V Power supply transmitter	2	2
30		Vcc1	+3.3V Power supply	2	2
31	LVTTL-I	LPMode	Low Power Mode	3	
32		GND	Ground	1	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3	
34	CML-I	Tx3n	Transmitter Inverted Data Input	3	
35		GND	Ground	1	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3	
37	CML-I	Tx1n	Transmitter Inverted Data Input	3	
38		GND	Ground	1	1

1.GND is the symbol for signal and supply (power) common for the QSFP28 module. All are common within

the QSFP28 module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.

2.Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Requirements defined for the host side of the Host Edge Card Connector are listed in Table 6. Recommended host board power supply filtering is shown in Figures 3 and 4. Vcc Rx Vcc1 and Vcc Tx may be internally connected within the QSFP28 Module in any combination. The connector pins are each rated for a maximum current of 500mA.

Mechanical Dimensions



Ordering information

Part Number	Data Rate	Fiber	Distance	Interface	Temp	DDMI
RYTQC40-4	100 Gbps	SMF	40km	LC	0 to +70°C	√